DECLARATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled METHOD AND STRUCTURE FOR A SELF-ALIGNED SILICIDED WORD LINE AND POLYSILICON PLUG DURING THE FORMATION OF A SEMICONDUCTOR DEVICE, the specification of which:

X is attached hereto. was filed on	, as Application Serial No	0	
I hereby state that I has specification, including the claims.		d the contents of the abo	ve identified
I acknowledge the duty to matter claimed in this application a Regulations, § 1.56.		is material to patentability of Title 37 of the Code of Federa	-
I hereby claim the benefit under Title 35 of the United States this application is not disclosed in paragraph of Title 35 of the Unite which is material to patentability a became available between the filin filing date of this application.	s Code, § 120 and, insofar as the prior United States applied ed States Code, § 112, I acknowled to the Code of th	ication in the manner provide nowledge the duty to disclose Code of Federal Regulations,	the claims of ed by the first e information § 1.56 which
(Application Serial No.)	(Filing Date)	(Status)	
Send correspondence to:			
Kevin	D. Martin, Reg. 37,882		

Mail Stop 01-525 Micron Technology, Inc. 8000 S. Federal Way Boise, Idaho 83716 (208) 368-4516

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18 of the United States Code, § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first or sole	nventor: Fredrick D. Fishburn	
Inventor's Signature:	First, Middle Initial, Last)	
Date:	7/10/03	
Residence Address: City, State, Country:	1608 N. 8 th Street Boise, Idaho United States of America	
Citizenship: Post Office Address:	United States Same as residence address	
Full name of first or sole inventor: Terrence B. McDaniel		
Inventor's Signature:	(First, Middle Initial, Last)	
Date:	7.10.03	
Residence Address: City, State, Country:	1568 S. Riverstone Lane Apt# 303 Boise, Idaho United States of America	
Citizenship: Post Office Address:	United States Same as residence address	
Full name of first or sole inventor: Richard H. Lane		
Inventor's Signature:	(First, Middle Initial, Last)	
Date:	7/10/03	
Residence Address: City, State, Country:	204 W State Street Boise, Idaho United States of America	
Citizenship: Post Office Address:	United States Same as residence address	

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	Atty. Docket: 2003-0076.00/US
Fredrick D. Fishburn Terrence B. McDaniel Richard H. Lane	Certificate of Express Mailing (37 CFR § 1.10) "Express Mail" mail label number: ET658403415US Date of Deposit: リリント カルろ
Serial No.:	I hereby certify that this paper is being deposited with the United States Postal Service "Express Mail Post Office to Addressee"
Filed: Concurrently Herewith	service under 37 CFR §1.10 on the date indicated above and is
For: METHOD AND STRUCTURE FOR A SELF- ALIGNED SILICIDED WORD LINE AND POLYSILICON PLUG DURING THE FORMATION OF A SEMICONDUCTOR DEVICE	addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. Signature

ELECTION UNDER 37 C.F.R. §§ 3.71 AND 3.73 AND POWER OF ATTORNEY

Mail Stop Patent Application Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The undersigned, being Assignee of the entire interest in the above-identified application by virtue of an Assignment recorded in the United States Patent and Trademark Office as set forth below or filed herewith, hereby elects, under 37 C.F.R. § 3.71, to prosecute the application to the exclusion of the inventor.

The Assignee hereby revokes any previous Powers of Attorney and appoints: Charles B. Brantley, II, Reg. No. 38,086; Michael L. Lynch, Reg. No. 30,871; Kevin D. Martin, Reg. No. 37,882; David J. Paul, Reg. No. 34,692; and Russel D. Slifer, Reg. No. 39,838, as its attorney or agent, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, to receive any Letters Patent, and for one year after issuance of such Letters Patent to file any request for a certificate of correction that may be deemed appropriate.

Pursuant to 37 C.F.R. § 3.73, the undersigned duly authorized designee of Assignee certifies that the evidentiary documents have been reviewed, specifically the Assignment to MICRON TECHNOLOGY, INC., referenced below, and certifies that to the best of my knowledge and belief, title remains in the name of the Assignee.

Assignment:	Please direct all communications as follows:
X Filed concurrently herewith for recording, a copy of which is attached hereto. Previously recorded on:, at Reel:, Frame:	Kevin D. Martin MICRON TECHNOLOGY, INC. Mail Stop 01-525 8000 S. Federal Way Boise, ID 83716-9632 (208) 368-4516
	ASSIGNEE: MICRON TECHNOLOGY, INC.
Date: 7-10-03	By: Michael L. Lynch, Reg. No. 30,871